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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



In re Application of:

Vitaly LAGOON et al.

Serial No.: 09/939,743

Filed: August 28, 2001

For: Method For Providing Bitwise Constraints  
For Test Generation

Examiner:

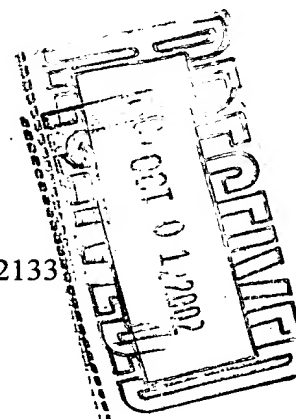
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Group Art Unit: 2133

Attorney

Docket: 24771

Previously V02/14



**INFORMATION DISCLOSURE STATEMENT**

Director of the United States Patent and Trademark Office  
US Patent and Trademark Office  
Washington, D.C. 20231

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Sir:

Enclosed is a PTO Form SB/08A which lists citations which may be material to the patentability and examination of the above identified application. Also enclosed are copies of the references cited. These are submitted in compliance with the duty of disclosure defined in 37 CFR 1.56. The Examiner is requested to make these citations of official record in this application.

This Information Disclosure Statement under 37 CFR 1.56 is not to be construed as a representation that a search has been made, that additional matter which is material to the examination of this application does not exist, or that any or more of these citations constitutes prior art.

Respectfully submitted,

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Date: September 18, 2002

